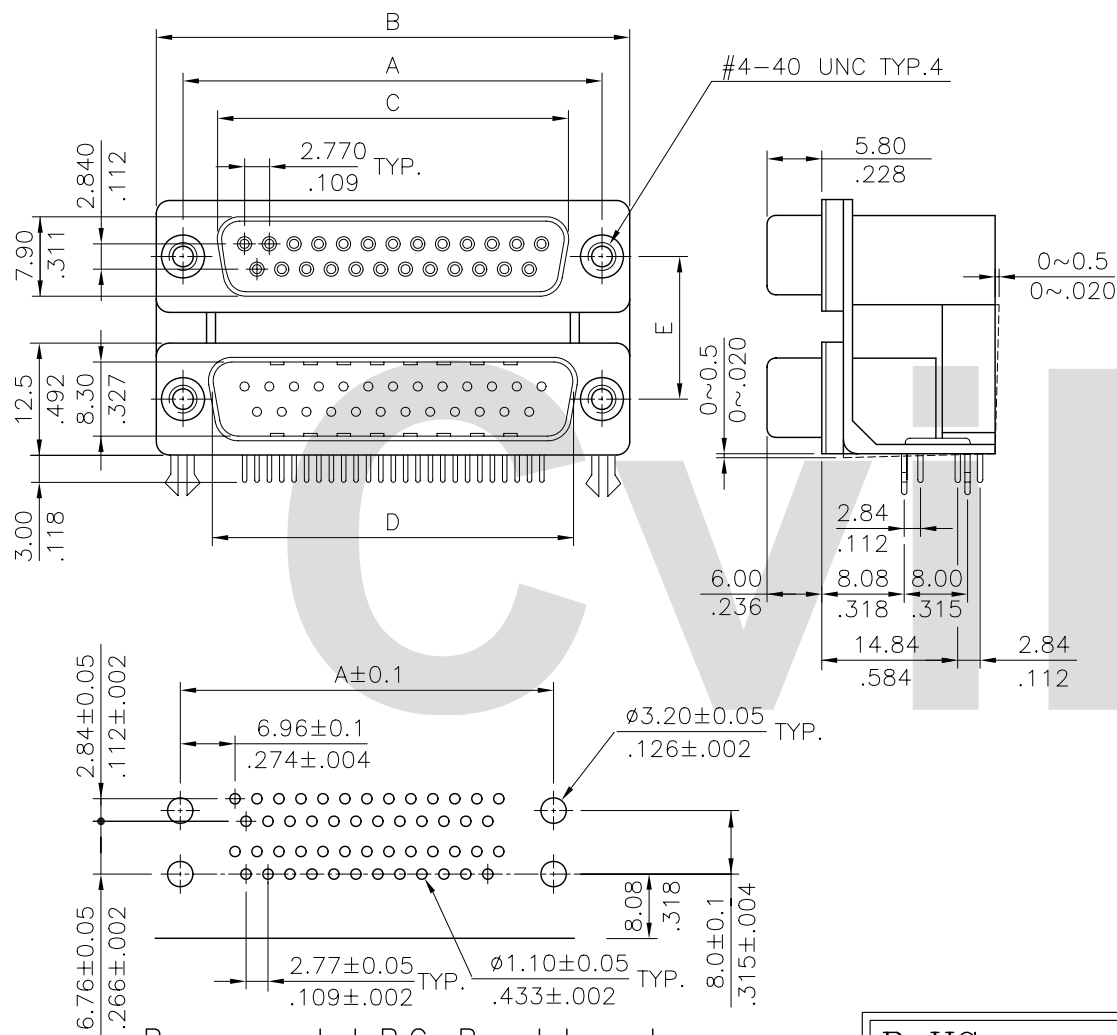


- * Insulator: Glass filled Polyester UL 94V-0
- * Contacts: Copper alloy
- * Shell: Steel, Tin plated as standard

CD81	V1	SP	A	A	C
①	②	③	④	⑤	⑥

- ① Series No.
- ② Configuration: V1= 9 To 9 ; V2= 15 To 15
V3= 25 To 25 ; V7= 37 To 37
- ③ Concaat type: SP= Socket to Plug
- ④ Plating option:
A= Selective Gold flash plated over 1.27 μ m(50 μ) Nickel
B= Selective 0.38 μ m(15 μ ”) Gold plated over
1.27 μ m(50 μ ”) Nickel
C= Selective 0.76 μ m(30 μ ”) Gold plated over
1.27 μ m(50 μ ”) Nickel
- ⑤ Row spacing: A= Dim. E 15.88mm
B= Dim. E 19.05mm
C= Dim. E 22.86mm
- ⑥ Other Option:
C= Riveted ground hooks with #4-40UNC open
S= Riveted ground hooks Pre-Loaded #4-40UNC Hex screws



Recommended P.C. Board Layout

RoHS Compliant

CVILUX P/N	Dimension				
	A	B	C	D	E
CD81V1SP*A*	25.00(.984)	30.8(1.213)	16.30(.642)	16.92(.666)	15.88(.625)
CD81V2SP*A*	33.32(1.312)	39.2(1.543)	24.60(.969)	25.25(.994)	
CD81V3SP*A*	47.04(1.852)	53.1(2.091)	38.30(1.508)	38.96(1.534)	
CD81V7SP*A*	63.50(2.500)	69.4(2.732)	54.80(2.157)	55.42(2.182)	
CD81V1SP*B*	25.00(.984)	30.8(1.213)	16.30(.642)	16.92(.666)	19.05(.750)
CD81V2SP*B*	33.32(1.312)	39.2(1.543)	24.60(.969)	25.25(.994)	
CD81V3SP*B*	47.04(1.852)	53.1(2.091)	38.30(1.508)	38.96(1.534)	
CD81V7SP*B*	63.50(2.500)	69.4(2.732)	54.80(2.157)	55.42(2.182)	
CD81V1SP*C*	25.00(.984)	30.8(1.213)	16.30(.642)	16.92(.666)	22.86(.900)
CD81V2SP*C*	33.32(1.312)	39.2(1.543)	24.60(.969)	25.25(.994)	
CD81V3SP*C*	47.04(1.852)	53.1(2.091)	38.30(1.508)	38.96(1.534)	
CD81V7SP*C*	63.50(2.500)	69.4(2.732)	54.80(2.157)	55.42(2.182)	

4						DATE	UNIT: mm / inch		TITLE: STACKED SIDE ENTRY BOARD MOUNT D-SUB	 瀚荃股份有限公司 CviLux Corporation			
3				DRAWN BY:	Sun	12/30 04'	TOLERANCE UNLESS OTHERWISE SPECIFIED						
2				ENGINEER:	Eisley	12/31 04'	.X ± 0.30/.012	X.* ± 1"	MATERIAL:				
1	Sun	12/30/04	ECN04519	CHECKED BY:	Alex	12/31 04'	.XX ± 0.20/.010	.X.* ±					
SYM	NAME	DATE	REVISIONS	APPROVED BY:	Alex	12/31 04'	.XXX ± 0.10/0.04	.XX.* ±	FINISH:	DRAWING NO.	CD8103SF	PART NO.	CD81V*SP***
										SCALE	2 / 1	SHEET	1 OF 1